

D+	D-	State
0	0	Se0
0	1	Low speed detect
1	0	Full speed detect
1	1	Bus error

FIG.1

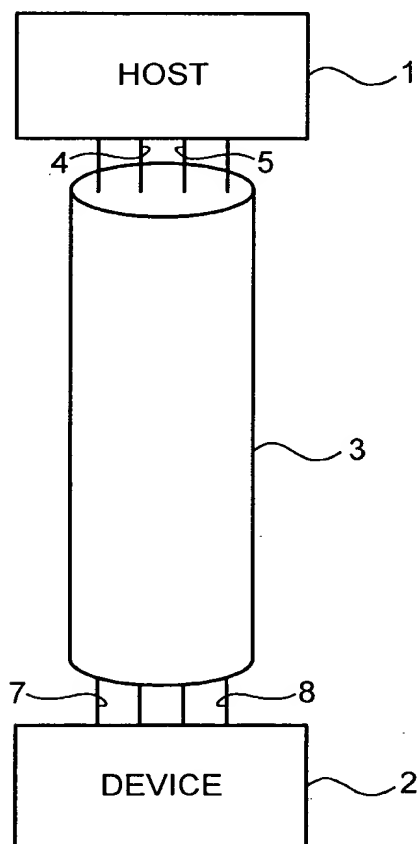


FIG.2

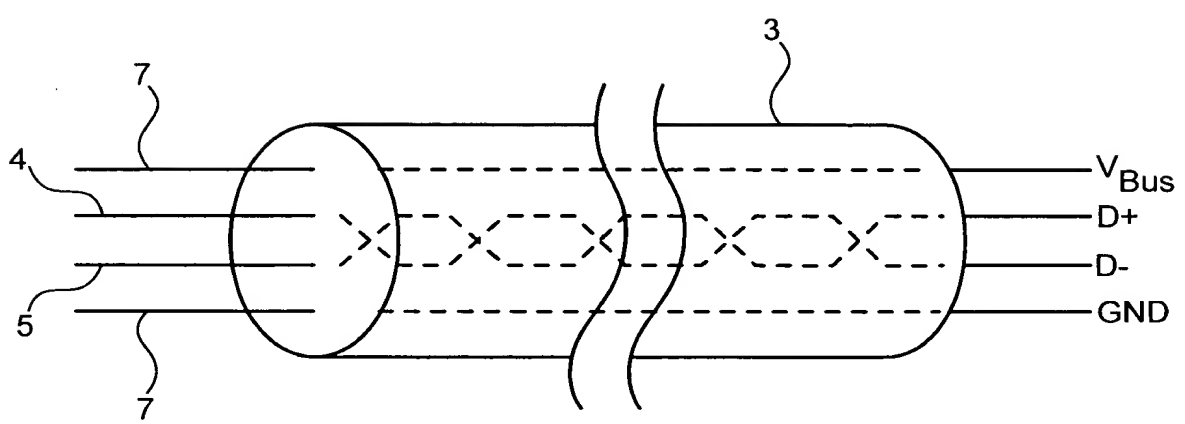


FIG. 3

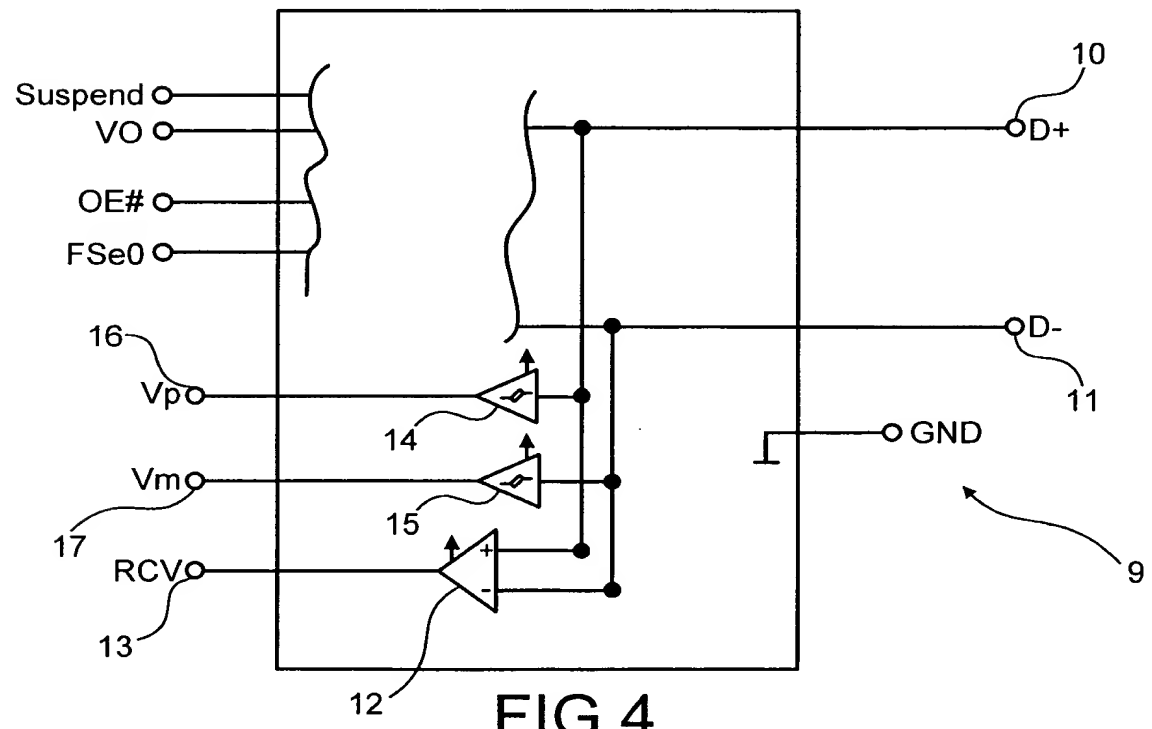


FIG. 4
PRIOR ART

FIG. 3

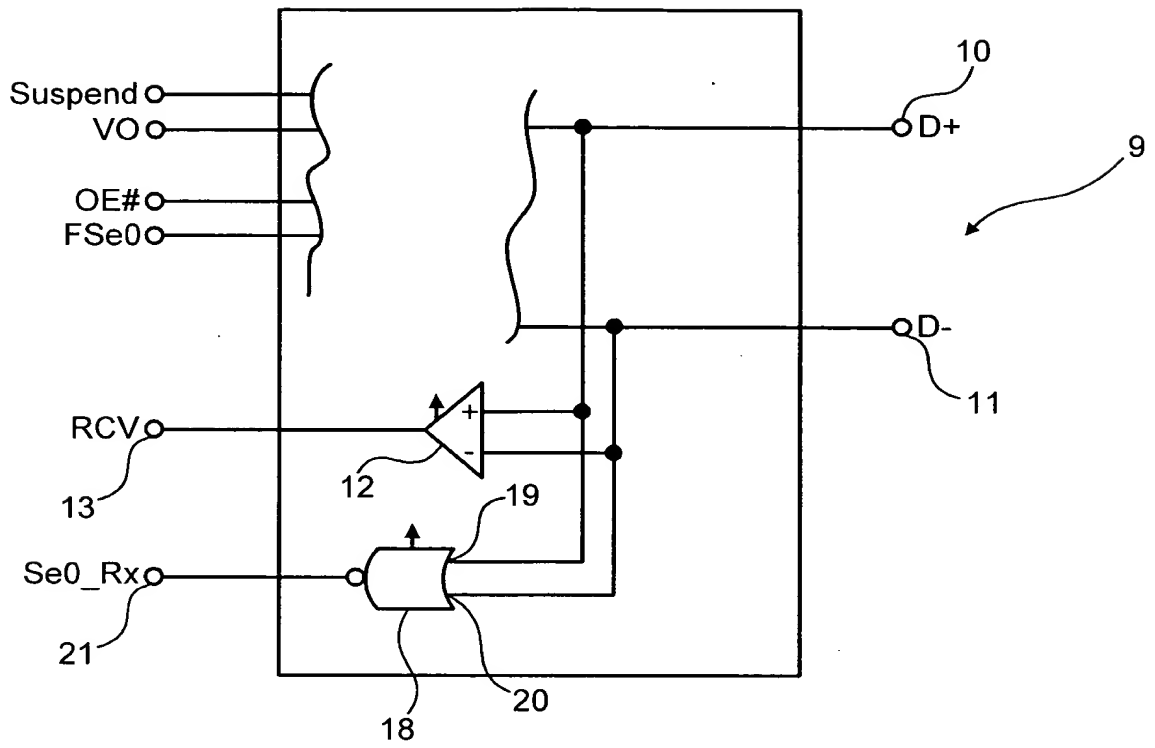


FIG.5

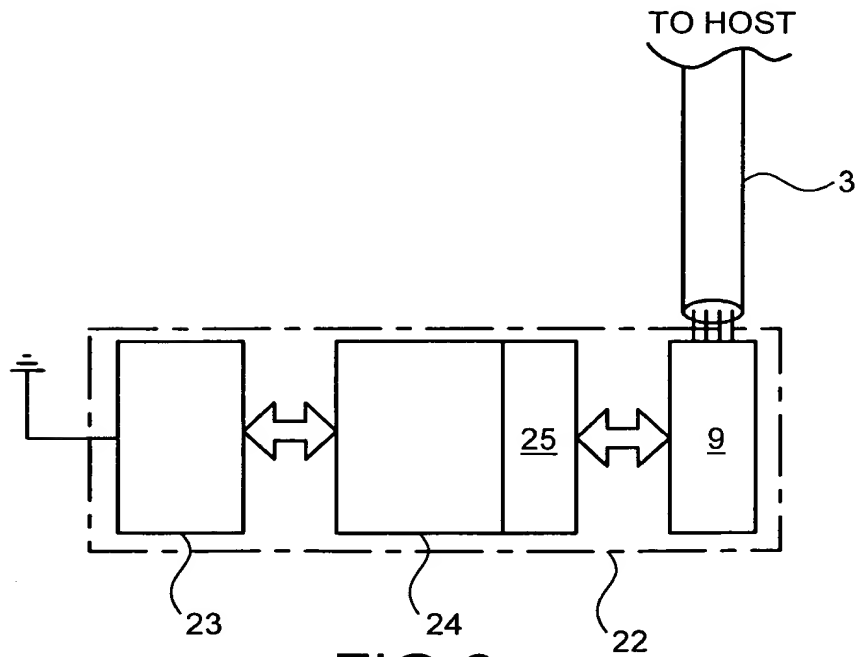


FIG.6